

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Chatty et al.

Serial No.: 10/708,912

Filed: March 31, 2004

Group Art Unit: 2813

Examiner: Pham, Thanhha S.

Atty. Docket No.: BUR920030180US1

For: METHOD FOR CREATING A SELF-ALIGNED SOI DIODE BY REMOVING A
POLYSILICON GATE DURING PROCESSING

AMENDMENT UNDER 37 C.F.R. §1.111

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed May 30, 2006, setting a three-month statutory period for response. Therefore, this amendment is timely filed. Please amend the above-identified patent application as follows: